

1084

Figure 1

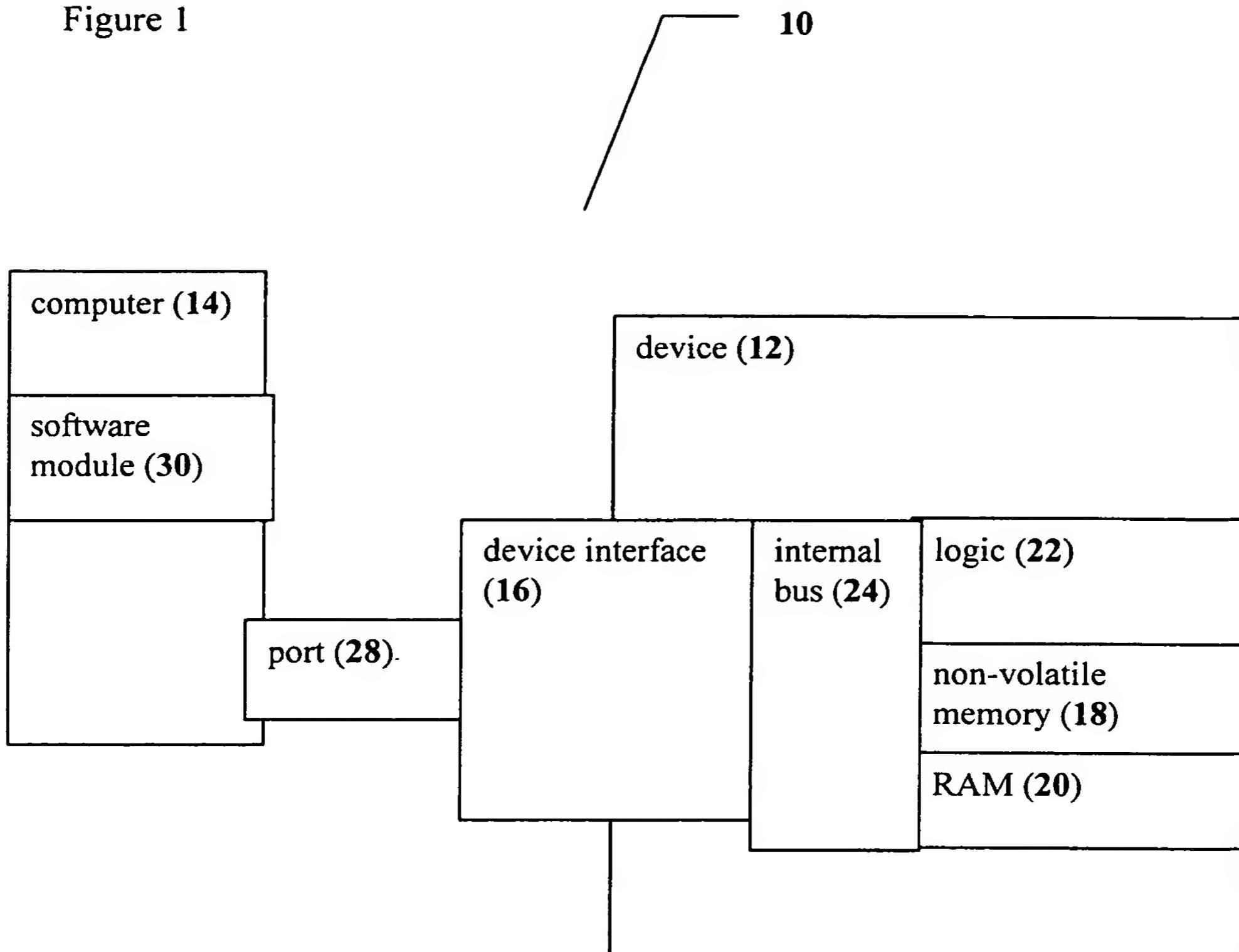


Figure 2

cont. from page 22 of 22 = 22 cont. from 22 of 22 = 22

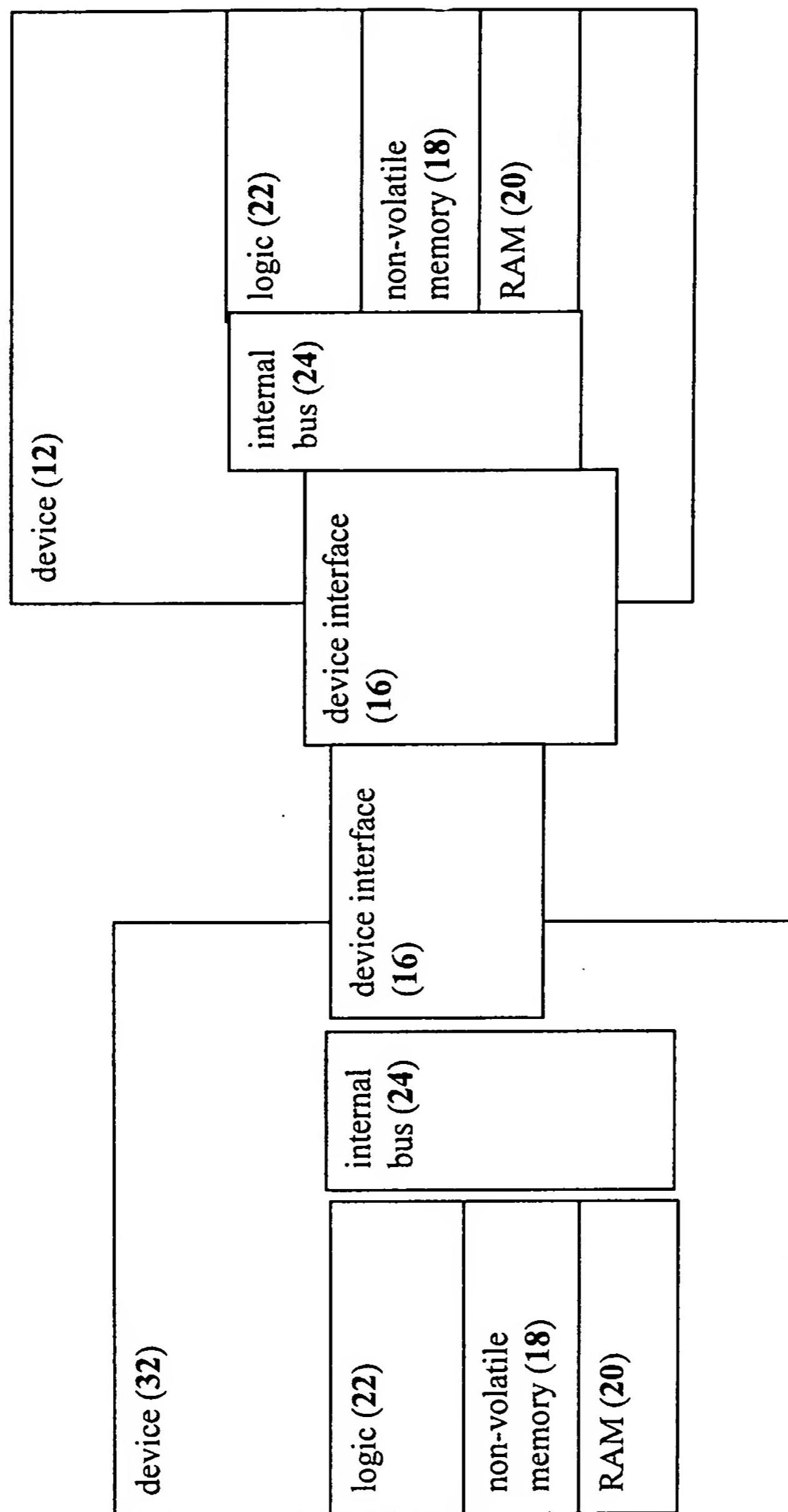


Figure 3

device
internal bus
logic
non-volatile
memory
RAM
user interface
display
information entry component

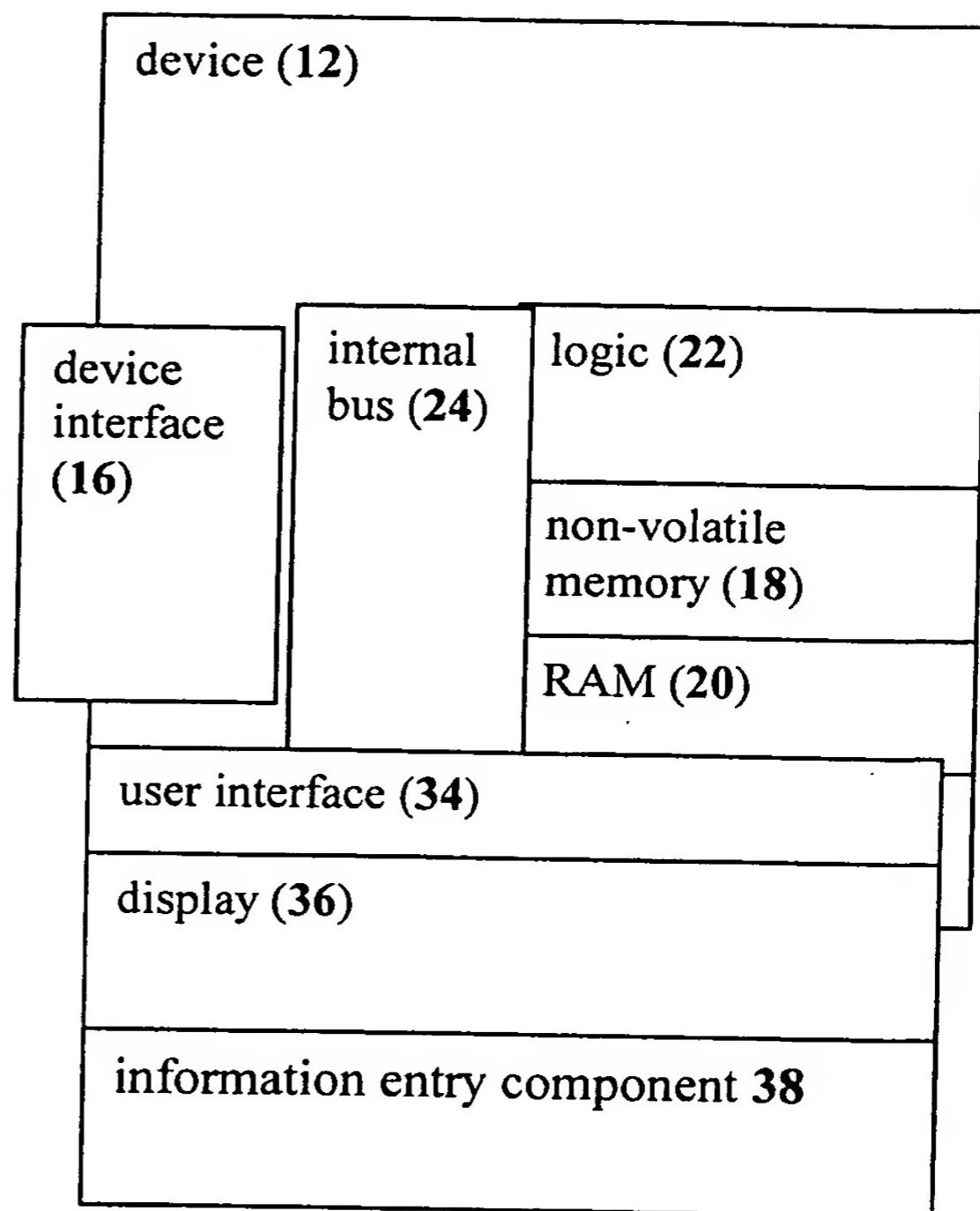


Figure 4

